

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

France

200,842	D. Pilost, F. de Saint-Michel, J. Belloc, J. Cholat-Namy, M. Choquet, S. Huon, and V. Spagnol	Method and System for Controlling a Network of Modems
288,649	A. Gach, M. Peyronnenc, and Y. Hartmann	Memory Control Subsystem
310,728	M. Vergaeghe and P. Hauviller	Device for Extending the Resolution of an N-Bit Resistive Digital to Analog Converter to a (N+P)-Bit Digital to Analog
321,628	M. Peyronnenc, P. Huon, R. Glaise, and Y. Hartmann	An Efficient Interface for Manet Main Store

Germany

9,202,678.8	W. Loef	Clean Room Box
68,903,950.6	H. Weiss, H. K. Wickramasinghe, J. Greschner, O. Wolter, T. Bayer, and Y. Martin	Method of Producing Ultrafine Silicon Tips for the AFM/STM Profilometry
68,903,951.4	G. Kraus, H. Weiss, J. Greschner, O. Wolter, and T. Bayer	Method of Producing Micromechanical Sensors for the AFM/STM Profilometry and Micromechanical AFM/STM Sensor Head
3,687,045,508	A. Brunsch, C. Schmidt, G. Trippel, H. Rink, and W. Steiner	Method and Arrangement for Improving the Dispersion of Magnetic Particles in a Magnetic Coating Compound

Japan

1,705,371	O. Watanuki and T. Kaneko	Speech Recognition Apparatus
-----------	---------------------------	------------------------------

Switzerland

324,044	C. A. Mueller, H. P. Wolf, K. A. Mueller, and P. Chaudhari	A Field-Effect Device with a Superconducting Channel
---------	--	--

United Kingdom

2,021,306	E. Chamberlain and M. D. Oates	Nettle Sales Point
-----------	--------------------------------	--------------------

United States

5,146,422	K. K. Maass and D. T. Shen	Reduced Execution Time Convert to Binary Circuit
5,146,450	P. E. Brooks, J. R. Reidenbach, and M. E. Troutman	Method and Apparatus for Bearing to Comb Attachment

5,146,460	D. F. Ackerman, D. R. Bender, S. S. Chu, G. R. Diebert, G. G. Hallock, D. E. Lackey, R. G. Sheldon, and T. A. Stranko	Logic Simulation Using a Hardware Accelerator Together with an Automated Error Event Isolation and Trace Facility
5,146,514	Y. Birk	Efficient Single-Hop Directional Multichannel System
5,146,540	K. S. Natarajan	Processor Allocation Method and Apparatus for Multiprocessor Execution of a Constraint Satisfaction Search
5,146,543	S. Vassiliadis and G. G. Pechanek	Scalable Neural Array Processor
5,146,551	A. Miyazawa	System for Generating a Circular or Elliptic Arc on a Digital Display
5,146,552	E. Cassorla, J. P. de Vries, M. E. Moran, J. J. Slater, and J. Unger	Method for Associating Annotation with Electronically Published Material
5,146,562	R. W. Kukla	Patient Care Communication System
5,146,566	R. L. Hollis, Jr. and S. E. Salcudean	Input/Output System for Computer Use Interface Using Magnetic Levitation
5,146,570	P. D. Hester and W. M. Johnson	System Executing Branch-with-Execute Instruction Resulting in Next Successive Instruction Being Executed While Specified Target Instruction is Prefetched for Following Execution
5,146,572	R. N. Bailey and R. L. Mansfield	Multiple Data Format Interface
5,146,575	T. J. Nolan, Jr.	Implementing Privilege on Microprocessor Systems for Use in Software Asset Protection
5,146,576	B. C. Beardsley, M. T. Benhase, and S. K. Candelaria	Managing High Speed Slow Access Channel to Slow Speed Cyclic System Data Transfer
5,146,582	R. M. Begun	Data Processing System with Means to Convert Burst Operations into Memory Pipelined Operations
5,146,585	T. B. Smith III	Synchronized Fault Tolerant Clocks for Multiprocessor Systems
5,146,590	R. A. Lorie and H. C. Young	Method for Sorting Using Approximate Key Distribution in a Distributed System
5,146,593	R. T. Brandle, D. L. Goodliffe, D. E. Keith, R. A. Robinette, R. C. Sizemore, G. J. Smithwick, and A. J. Zappavigna	Procedure Call Interface
5,146,605	B. L. Beukema, D. W. Marquart, and R. D. Morse	Direct Control Facility for Multiprocessor Network
5,146,674	R. F. Frankeny, K. Hermann, R. L. Imken, and J. LaTorre	Manufacturing Process of a High Density Substrate Design
5,147,084	J. R. Behun, A. J. Call, F. F. Cappel, M. S. Cole, K. G. Hoebener, B. T. Klingel, and J. C. Milliken	Interconnection Structure and Test Method
5,147,484	D. A. Chance, G. G. Hougham, and D. B. Goland	Method for Producing Multi-Layer Ceramic Substrates with Oxidation Resistant Metalization
5,147,741	C. J. Araps, J. A. Casey, R. S. Divakaruni, S. M. Kandetzke, and C. A. Lotsko	Phenyl-Endcapped Depolymerizable Polymer
5,148,003	J. Haj-Ali-Ahmadi, J. A. Frankeny, R. F. Frankeny, A. B. Habich, K. Hermann, and R. E. Hunt	Modular Test Oven
5,148,059	C.-L. Chen, P. W. Cook, and L. M. Terman	CMOS and ECL Logic Circuit Requiring No Interface Circuitry
5,148,261	S. K. Kang, M. J. Palmer, T. C. Reiley, and R. D. Topa	Thermocompression Bonding in Integrated Circuit Packaging
5,148,340	R. B. Watrous	Bifurcated Air Bearing Actuator Assembly for a Disk Drive System
5,148,479	R. F. Bird, I. S. Gopal, P. A. Janson, S. Kutten, R. A. Molva, and M. M. Yung	Authentication Protocols in Communication Networks
5,148,481	D. G. Abraham, S. G. Aden, T. W. Arnold, S. W. Neckyfarow, and W. S. Rohland	Transaction System Security Method and Apparatus
5,148,515	S. Vassiliadis and G. G. Pechanek	Scalable Neural Array Processor and Method
5,148,521	T. J. Ebberts and D. G. Gibbons	Pick Data Queue for Pipelined Graphics System
5,148,527	C. Basso, G. Lebizay, J.-M. Munier, and A. Pauporte	Interface for Independently Establishing a Link and Transmitting High Level Commands Including Logical Addresses from Dedicated Microprocessor to Shared Intelligent Memory
5,148,534	L. D. Comerford	Hardware Cartridge Representing Verifiable, Use-Once Authorization
5,148,538	J. O. Celtruda, K. A. Hua, A. H. Hunt, L. Liu, J.-K. Peir, D. R. Pruett, and J. L. Temple III	Translation Look Ahead Based Cache Access
5,148,540	B. C. Beardsley, L. H. Boulia, and R. H. Vosacek	System with Backup of Data Storage Status and Device Identification for Enabling System Recovery After Powerloss
5,148,963	C. A. Hicks	Method for Solder Nozzle Height Sensing
5,149,409	R. H. Ahlert, J. K. Howard, and S. E. Lambert	Process for Fabricating Thin Film Metal Alloy Magnetic Recording Disks to Selectively Variable Coercivities

5,149,918	A. F. Kozik and D. M. Taylor	Touch Sensitive Overlay
5,149,919	E. C. Greanias, F. L. Stein, R. Donaldson, and M. Gray	Stylus Sensing System
5,149,974	S. J. Kirch, J. P. Levin, and A. Wagner	Gas Delivery for Ion Beam Deposition and Etching
5,150,040	H. P. Byrnes and R. Wahl	Dual-Mode Z-Stage
5,150,245	W. T. Smithgall	Multiprocessor Computer with Optical Data Switch
5,150,279	C. A. Collins and J. A. McDonald	High Performance Computer System with Platters and Unidirectional Storage Modules Therebetween
5,150,312	B. J. Beitel and R. D. Gordon	Animation Processor Method and Apparatus
5,150,328	F. J. Aichelmann, Jr.	Memory Organization with Arrays Having an Alternate Data Port Facility
5,150,366	P. H. Bardell, Jr. and W. H. McAnney	Reduced Delay Circuits for Shift Register Latch Scan Strings
5,150,392	F. J. Hohn and M. A. McCord	X-Ray Mask Containing a Cantilevered Tip for Gap Control and Alignment
5,150,437	F. Gfeller	Electro-Optical Scanner
5,150,457	J. L. Behm and W.-H. Chiang	Enhanced Visualization Using Translucent Contour Surfaces
5,150,470	T. N. Hicks and M. NguyenPhu	Data Processing System with Instruction Queue Having Tags Indicating Outstanding Data Status
5,150,472	T. E. Blank, D. F. Ferguson, J. A. Frey, A. Pruscino, R. R. Rogers, and E. Rahm	Cache Management Method and Apparatus for Shared, Sequentially-Accessed, Data
5,150,512	M. R. Hatchett, A. R. Hearn, and C. M. Lacey	Method of Assembling a Disk File
5,151,041	L. Kaiser, E. Kimmerle, E. Mirbach, and F. Notter	Device for Connecting and Disconnecting First and Second Electrical Connector Parts
5,151,169	G. T. Galyon, G. M. Jordhamo, and L. H. Wirtz	Continuous Anodizing of a Cylindrical Aluminum Surface
5,151,294	H. E. Bleich, L. P. Franco, and I. L. Sanders	Method for Forming a Protective Film on a Slider Air Bearing Surface in a Magnetic Recording Disk File
5,151,304	K.-W. Lee	Structure and Method for Enhancing Adhesion to a Polyimide Surface
5,151,383	B. S. Meyerson, B. A. Scott, and D. J. Wolford, Jr.	Method for Producing High Energy Electroluminescent Devices
5,151,559	H. W. Conru, G. H. Irish, F. J. Pakulski, W. J. Slattey, S. G. Starr, and W. C. Ward	Planarized Thin Film Surface Covered Wire Bonded Semiconductor Package
5,151,594	G. M. McClelland	Subpicosecond Atomic and Molecular Motion Detection and Signal Transmission by Field Emission
5,151,619	J. S. Austin, R. A. Piro, and D. W. Stout	CMOS Off Chip Driver Circuit
5,151,948	D. G. Lyke and M. Ominsky	System and Method for Processing Documents Having Amounts Recorded Thereon
5,151,977	K. J. Fredericks and K. R. Lynch	Managing a Serial Link in an Input/Output System Which Indicates Link Status by Continuous Sequences of Characters between Data Frames
5,151,981	D. W. Westcott and V. White	Instruction Sampling Instrumentation
5,151,987	R. L. Abraham, M. P. Priven, and T. P. Moorman	Recovery Objects in an Object Oriented Computing Environment
5,151,989	D. W. Johnson, A. A. Shaheen-Gouda, and T. A. Smith	Directory Cache Management in a Distributed Data Processing System
5,151,990	J. D. Allen and S. A. King	Nonvolatile Memory Management in a Data Processing Network
5,152,878	M. Datta and K. U. Rocheleau	Method for Electrochemical Cleaning of Metal Residue on Molybdenum Masks
5,152,880	T. R. O'Toole and A. Viehbeck	Electrodeposition of a Polymer
5,153,307	R. T. Gleason and H. G. Linde	Stabilization of Polyamide Alkyl Ester Solutions
5,153,408	E. F. Handford, J. M. Harvilchuck, M. J. Interrante, R. A. Jackson, R. N. Master, S. K. Ray, W. E. Sablinski, and T. A. Wassick	Novel Method and Structure for Repairing Electrical Lines
5,153,453	M. M. Walters	High Voltage Majority Carrier Rectifier
5,153,472	J. P. Karidis, G. McVicker, J. P. Pawletko, and R. R. Comulada, Jr.	Probe Positioning Actuator
5,153,494	R. L. Hollis, Jr.	Ultrafast Electro-Dynamic X, Y and Theta Positioning Stage
5,153,504	T. J. Bonnet and J. H. Kirk	Pneumatically Actuated Hold Down Gate
5,153,813	G. S. Oehrlein and G. W. Rubloff	High Area Capacitor Formation Using Dry Etching
5,153,890	G.-L. Bona, W. Heuberger, P. Roentgen, and P. Unger	Semiconductor Device Comprising a Layered Structure Grown on a Structured Substrate

5,153,936	H. M. Morris, C. A. Parks, R. E. Probst, Y. Tsalalikhin, G. L. Youngs, and D. Rajagopal	Dual Density Digital Image System
5,153,986	J. M. Brauer, F. R. Christie, W. H. Lawrence, A. A. Mehta, J. D. Reid, and W. J. Summa	Method for Fabricating Metal Core Layers for a Multi-Layer Circuit Board
5,154,021	S. G. Bombardier and J. R. Day	Pneumatic Pad Conditioner
5,154,022	P. A. Chalco, H. D. Euler, and R. L. Rohr	High Precision Micromachining of Very Fine Features
5,154,333	T. L. Bauer, W. A. Cavaliere, D. C. Linnell, and R. R. Ruckel	Jaw Cleaving Device
5,154,514	J. P. Gambino, L. L. Hsu, M. A. Lee, K. Seshan, A. Sugerman, and F. E. Turene	On-Chip Temperature Sensor Utilizing a Schottky Barrier Diode Structure
5,155,372	A. J. Bowen, D. L. Erickson, D. W. Jewell, and V. R. Koka	Optical Inspection System Utilizing Wedge Shaped Spatial Filter
5,155,412	T.-H. P. Chang, D. P. Kern, and L. P. Muray	Method for Selectively Scaling a Field Emission Electron Gun and Device Formed Thereby
5,155,478	K. Sekiya and Y. Shiraishi	Method and Apparatus for Converting Gray Scale
5,155,486	P. J. Murfet and C. N. Wallis	Asynchronous Serial Data Receiver with Capability for Sampling the Mid-Point of Data Bits
5,155,572	D. Bonneau, M. Combes, A. J. Dally, P. Mollier, S. Ogura, and P. Tannhof	Vertical Isolated-Collector PNP Transistor Structure
5,155,577	D. A. Chance, T. R. Dinger, D. P. Lapotin, and W. V. Vilkelis	Integrated Circuit Carriers and a Method for Making Engineering Changes in Said Carriers
5,155,642	O. Voegeli	Anisotropy Configuration for Longitudinally Constrained Magnetoiresistive Transducers
5,155,652	J. S. Logan, R. R. Ruckel, R. E. Tompkins, and R. P. Westerfield, Jr.	Temperature Cycling Ceramic Electrostatic Chuck
5,155,657	G. S. Oehrlein, V. V. Patel, A. Grill, R. T. Hodgson, and G. W. Rubloff	High Area Capacitor Formation Using Material Dependent Etching
5,155,676	J. H. Spreen	Gapped/Ungapped Magnetic Core
5,155,677	J. M. Kurtzberg and M. Levanoni	Manufacturing Process Optimizations
5,155,678	T. Fukumoto, T. Funahasi, G. Schweikert, H. Scofield, T. E. Walker, and J. W. Young, Jr.	Data Availability in Restartable Data Base System
5,155,749	V. DiMilia and J. M. Warlaumont	Variable Magnification Mask for X-Ray Lithography
5,155,763	J. P. Bigus, R. A. Diedrich, and C. E. Smith	Look Ahead Method and Apparatus for Predictive Dialing Using a Neural Network
5,155,777	M. Angelopoulos, M. S. Cohen, A. T. S. Pomerene, and D. L. Rogers	Scattered Light Blocking Layer for Optoelectronic Receivers
5,155,786	M. E. Ecker and L. Jacobowitz	Apparatus and a Method for an Optical Fiber Interface
5,155,807	B. J. Blevins, W. G. Kulpa, and J. R. Mathis	Multi-Processor Communications Channel Utilizing Random Access/Sequential Access Memories
5,155,809	E. D. Baker, J. M. Dinwiddie, Jr., L. E. Grice, J. M. Joyce, J. M. Loffredo, and K. R. Sanderson	Uncoupling a Central Processing Unit from its Associated Hardware for Interaction with Data Handling Apparatus Alien to the Operating System Controlling Said Unit and Hardware
5,155,814	B. C. Beardsley, K. A. Bello, M. T. Benhase, F. J. Clark, P. W. Hunter, and D. M. Nordahl	Nonsynchronous Channel/DASD Communication System
5,155,831	P. G. Emma, J. W. Knight, J. H. Pomerene, R. N. Rechtschaffen, and F. J. Sparacio	Data Processing System with Fast Queue Store Interposed between Store-Through Caches and a Main Memory
5,155,844	C. Cheng, S. L. Porter, W. C. Richardson, and P. J. Roy	Background Memory Test during System Start Up
5,155,850	F. L. Janis, M. L. Williams, and D. S. Wang	Method and System for Maintaining a Time Frame Selective Document History Log in a Data Processing System
5,155,856	D. W. Bock, P. Mannherz, P. Rudolph, and H. Schulze-Schölling	Arrangement in a Self-Guarding Data Processing System for System Initialization and Reset
5,156,730	A. C. Bhatt, M. T. Freeman, J. J. Konrad, and N. G. Shah	Electrode Array and Use Thereof
5,156,784	R. I. Fuentes	Method for Fabricating a Silicon Carbide Film to Provide Improved Radiation Hardness
5,157,286	N. D. Butler and R. B. Harrison	Conditional Clocking of the Second Latch of a Shift Register Ratch
5,157,384	E. C. Greanias, G. F. Verrier, G. W. Arbeitman, A. R. Tannenbaum, and Y. L. An	Advanced User Interface
5,157,477	D. A. Chance	Matched Impedance Vertical Conductors in Multilevel Dielectric Laminated Wiring
5,157,488	W. B. Pennebaker	Adaptive Quantization Within the JPEG Sequential Mode
5,157,530	M. L. Loeb and G. R. Stilwell, Jr.	Optical Fiber System

5,157,531	H. R. Foglia	Hybrid Transmission Network
5,157,543	T. Fukuzawa, S. S. Kano, and Y. Takahashi	Optical Beam Deflector
5,157,592	M. M. Walters	DC-DC Converter with Adaptive Zero-Voltage Switching
5,157,634	S. H. Dhong, R. L. Franch, and W. Hwang	DRAM Having Extended Refresh Time
5,157,635	W. F. Ellis, E. Klink, and K. Najmann	Input Signal Redriver for Semiconductor Modules
5,157,667	A. Carusone, Jr., A. W. Garrigan, W. Hunsinger, G. T. Moffitt, D. E. Spencer, and J. M. Taylor	Methods and Apparatus for Performing Fault Isolation and Failure Analysis in Link-Connected Systems
5,157,736	S. K. Boyer, R. G. Casey, A. M. Miller, B. Oudot, and K. S. Zilles	Apparatus and Method for Optical Recognition of Chemical Graphics
5,157,763	A. M. Peters and R. J. Torres	Visually Assisted Method for Transfer of Data Within an Application or from a Source Application to a Receiving Application
5,157,765	Y. Birk and J. M. McCrossin	Method and Apparatus for Pipelined Parallel Rasterization
5,157,766	N. D. Butler and A. C. Gay	Boundary Drawing and Area Filling Logic for a Display System
5,157,770	B. C. Beardsley and M. T. Benhase	Nonsynchronous DASD Control
5,158,645	K. L. Covert, C. C. Emmons, E. Foster, R. W. Malek, V. R. Markovich, S. L. Tisdale, and C. L. Tytran	Method of External Circuitization of a Circuit Panel
5,158,690	J. S. Batchelder, D. W. Cooper, D. M. DeCain, and W. W. Hildenbrand	Thermophoretic Filtering of Liquids
5,158,896	J. H. Burroughes, M. S. Milshtein, M. A. Tischler, S. Tiwari, and S. L. Wright	Method for Fabricating Group III-V Heterostructure Devices Having Self-Aligned Graded Contact Diffusion Regions
5,159,170	J. P. Levin and A. Wagner	Grid Structure for Reducing Current Density in Focussed Ion Beam
5,159,172	D. S. Goodman, A. E. Rosenbluth, R. E. Tibbetts, and J. S. Wilczynski	Optical Projection System
5,159,201	J. B. Frei	Shape Decomposition System and Method
5,159,409	A. F. Coneski, Y.-S. Lin, and G. B. Vander Gheynst	Substrate Machining Verifier
5,159,429	R. E. Bendoragel, K.-M. Kim, V. J. Silvestri, P. Smetana, T. H. Strudwick, and W. H. White	Semiconductor Device Structure Employing a Multi-Level Epitaxial Structure and Method of Manufacturing Same
5,159,508	A. Grill, C. T. Horng, B. S. Meyerson, V. V. Patel, and M. A. Russak	Magnetic Head Slider Having A Protective Coating Thereon
5,159,512	R. C. Evans, M. A. O'Connor, and J. R. Rossignac	Construction of Minkowski Sums and Derivatives Morphological Combinations of Arbitrary Polyhedra in CAD/CAM Systems
5,159,513	B. Diemy, B. A. Gurney, S. Metin, S. S. P. Parkin, and V. S. Speriosu	Magnetoresistive Sensor Based on the Spin Valve Effect
5,159,529	E. P. Lovgren, D. P. Rearick, and C. B. Wells	Composite Liquid Cooled Plate for Electronic Equipment
5,159,531	J. L. Horvath, M. H. McLeod, and C. Yakubowski	Multiple Radial Finger Contact Cooling Device
5,159,535	K. V. Desai, N. P. Franchak, R. H. Katyl, H. Kohn, T. A. Sholtes, V. G. Veeraraghavan, and C. G. Woychik	Method and Apparatus for Mounting a Flexible Film Semiconductor Chip Carrier on a Circuitized Substrate
5,159,592	C. E. Perkins	Network Address Management for a Wired Network Supporting Wireless Communication to a Plurality of Mobile Users
5,159,597	C. J. Monahan, M. L. Monahan, and D. L. Willson	Generic Error Recovery
5,159,629	G. P. Double and S. H. Weingart	Data Protection by Detection of Intrusion into Electronic Assemblies
5,159,662	C. R. Grady, F. D. Highland, C. T. Iwaskiw, and M. Pfeifer	System and Method for Building a Computer-Based Rete Pattern Matching Network
5,159,677	K. G. Rubsam, R. J. Schmalz, and E. S. Schulze	Method and System for Storing Data in and Retrieving Data from a Non-Main Storage Virtual Data Space
5,160,920	R. H. Harris	Fail Safe Display for Shelf Labels
5,160,987	W. D. Pricer, T. B. Faure, B. S. Meyerson, W. J. Nestork, and J. R. Turnbull, Jr.	Three-Dimensional Semiconductor Structures Formed from Planar Layers
5,161,087	J. A. Frankeny, R. F. Frankeny, K. Hermann, and R. Wustrau	Pivotal Heat Sink Assembly
5,161,089	R. C. Chu, M. J. Ellsworth, Jr., G. F. Goth, R. E. Simons, and M. L. Zumbrennen	Enhanced Multichip Module Cooling with Thermally Optimized Pistons and Closely Coupled Convective Cooling Channels, and Methods of Manufacturing the Same
5,161,133	M. D. Levenson	Magneto-Optical Storage System
5,161,156	R. I. Baum, C. H. Brotman, and J. W. Rymarczyk	Multiprocessing Packet Switching Connection System Having Provision for Error Correction and Recovery
5,161,163	D. C. Bossen, C.-L. Chen, and M.-Y. Hsiao	Method and Apparatus for Providing Error Correction to Symbol Level Codes

5,161,186	D. Dolev and C. Dwork	System for Secure and Private Communication in a Triple-Connected Network
5,161,214	M. Addink, C. Y. Hu, T. Leyba, J. J. Mullen, C. A. Till, and A. W. Holmes	Method and Apparatus for Document Image Management in a Case Processing System
5,161,219	J. O. Nicholson, J. C. O'Quin III, J. T. O'Quin II, and F. E. Strietelmeier	Computer System with Input/Output Cache
5,161,223	R. L. Abraham	Resumeable Batch Query for Processing Time Consuming Queries in an Object Oriented Database Management System
5,161,225	R. L. Abraham and M. P. Priven	Persistent Stream for Processing Time Consuming and Reusable Queries in an Object Oriented Database Management System
5,161,227	D. M. Dias, B. R. Iyer, and P. S. Yu	Multilevel Locking System and Method
5,161,299	E. V. Denison, V. N. Kahwaty, G. C. Stern, G. Steving, and R. P. Zammit	Method of Making a Magnetic Hybrid Interleaved Head
5,161,900	Z.-E. Bougathou and P. M. Herman	Self-Contained Low Power Fluid Bearing and Bearing Seal
5,162,003	R. A. Johnson, D. G. Landry, and H. B. Schwartz	Electrical Connector with a Constant Radius of Curvature Beam
5,162,133	J. W. Bartha, T. Bayer, J. Greschner, G. Kraus, and O. Wolter	Process for Fabricating Silicon Carbide Films with a Predetermined Stress
5,162,194	W. M. Morgan	Method of Providing a Printed Circuit Board with a Cover Coat
5,162,264	W. O. Haug, E. Klink, K. E. Kröll, T. Ludwig, H. Schettler, R. Stahl, and O. M. Wagner	Integrated Circuit Package
5,162,298	P. Chaudhari, C.-C. J. Chi, D. B. Dimos, J. D. Mannhart, and C. C. Tsuei	Grain Boundary Junction Devices Using High Tc Superconductors
5,162,668	C.-L. Chen, S. H. Dhong, and H. J. Shin	Small Dropout On-Chip Voltage Regulators with Boosted Power Supply
5,162,742	N. W. Atkins, P. J. Davies, and G. P. Suback	Method for Locating Electrical Shorts in Electronic Substrates
5,162,779	L. Lumelsky, L. Cheng, and S. M. Choi	Point Addressable Cursor for Stereo Raster Display
5,162,979	T. A. Anzelone, S. T. Cheung, M. E. Cohen, K. K. Cooke, J. R. Dewitt, M. S. Miller, J. H. Neer, E. M. Reid, and R. D. Wysong	Personal Computer Processor Card Interconnect System
5,162,992	M. L. Williams	Vector Relational Characteristical Object
5,163,054	M. E. Nagy	Method for Data Transmission Using a Modified High Level Data Link Control Protocol
5,163,096	C. E. Clark, M. G. Mall, C. A. Scalzi, and B. Sinha	Storage Protection Utilizing Public Storage Key Control
5,163,126	M. A. Einkauf, M. M. Klock, and N. T. Le	Method for Adaptively Providing Near Phong Grade Shading for Patterns in a Graphics Display System
5,163,146	B. C. Antanaitis, Jr., W. B. Emerson, Jr., and J. W. St. John	System Responsive to Interrupt Levels for Changing and Restoring Clock Speed by Changing and Restoring a Register Value
5,163,149	W. C. Brantley, Jr. and H. S. Stone	Combining Switch for Reducing Accesses to Memory and for Synchronizing Parallel Processes
5,163,363	K. H. Gilgert and B. W. Stoughton	Device for Multiple-Point Application of Equal Forces
5,163,834	F. W. Chapin, D. W. Dranchak, D. E. Engle, R. R. Hall, and T. G. Macek	High Density Connector
5,164,278	W. R. Brunsvold, C. J. Knors, M. W. Montgomery, W. M. Moreau, and K. M. Welsh	Speed Enhancers for Acid Sensitized Resists
5,164,711	W. G. Tuel, Jr.	System and Method for Generating Graphical Output
5,164,818	A. Blum, F. Gerth, M. Perske, and M. Schmidt	Removable VLSI Assembly
5,164,869	R. E. Fontana, Jr., L. T. Romankiw, and M. L. Williams III	Magnetic Recording Head with Integrated Magnetoresistive Element and Open Yoke
5,164,888	E. H. Stone, Jr.	Method and Structure for Implementing Dynamic Chip Burn-In
5,164,932	A. A. Fennema and R. A. Klem	Acquiring a Best Focus Using a Focus Signal Offset
5,164,988	S. M. Matyas, D. B. Johnson, A. V. Le, R. Prymak, W. C. Martin, W. S. Rohland, and J. D. Wilkins	Method to Establish and Enforce a Network Cryptographic Security Policy in a Public Key Cryptosystem
5,165,007	L. R. Bahl, P. V. DeSouza, R. L. Mercer, and M. A. Picheny	Feneme-Based Markov Models for Words
5,165,022	J. J. Erhard, R. E. Losinger, and D. J. Sucher	Channel and Control Unit Having a First I/O Program Protocol for Communication with a Main Processor and a Second Universal I/O Program Protocol for Communication with a Plurality of I/O Adapters
5,165,030	B. A. Barker	Method and System for Dynamic Creation of Data Stream Based Upon System Parameters and Operator Selections

5,165,031	E. A. Pruul, M. E. Vendryes, D. O. Wood, and M. Zielenski	Coordinated Handling of Error Codes and Information Describing Errors in a Commit Procedure
5,165,089	G. A. Jaquette and R. J. Justo	Optical Disk Players Having Readback Circuits with Noise Rejection and Read Signal Retiming
5,166,037	J. M. Atkinson, R. E. Darrow, J. D. Larnerd, and R. J. Moore	Method of Additive Circuitization of Circuit Boards with High Adhesion, Voidless Copper Leads
5,166,038	D. J. Clodgo, R. A. Previti-Kelly, and A. B. Stratton	Etch Resistant Pattern Formation via Interfacial Silylation Process
5,166,096	D. R. Cote, D. Stanasolovich, and R. A. Warren	Process for Fabricating Self-Aligned Contact Studs for Semiconductor Structures
5,166,547	J. Babakanian, J. W. Davis, M. S. Garvin, K. P. Liew, Y. Medan, and N. G. Thoma	Programmable DCVS Logic Circuits
5,166,552	A. G. Aipperspach, G. Boudon, A. H. Dansky, P. Mollier, I. Ong, N. Phan, B. Pluchino, S. J. Zier, and A. Zuckerman	Multi-Emitter BiCMOS Logic Circuit Family with Superior Performance
5,166,674	R. I. Baum, C. H. Brotman, and J. W. Rymarczyk	Multiprocessing Packet Switching Connection System Having Provision for Error Correction and Recovery
5,166,856	J. W. Liporace and J. A. Seirmarco	Electrostatic Chuck with Diamond Coating
5,166,871	L. B. Carroll and Scott C. Willis	Buck Switching Regulator with Tow Control Loops
5,166,888	H. Engelke	Fabrication of Particle Beam Masks
5,166,903	F. J. Aichelmann, Jr.	Memory Organization with Arrays Having an Alternate Data Port Facility
5,166,919	D. M. Eigler	Atomic Scale Electronic Switch
5,166,935	J. E. Bish	Method of Determining Correctness and Contents of Control Data Structures in Moving Media Data Storage Systems
5,166,989	B. N. Kurdi, G. T. Sincerbox, and J. M. Zavislan	Integrated Polarization Detection System
5,167,012	R. R. Hayes and L. Hsieh	Method for Performing Consistency Checks
5,167,023	A. M. de Nicolas and J. C. O'Quin III	Translating a Dynamic Transfer Control Instruction Address in a Simulated CPU Processor
5,167,029	R. G. Eikill and Q. G. Schmierer	Data Processing System and Associated Process Using Memory Cards Having Data Modify Functions Utilizing a Data Mask and an Internal Register
5,167,032	D. R. Martin, M. J. McDermott, S. D. Plumlee, R. H. Satin, and R. L. Wenger	Computer Controlled Optimized Pairing of Disk Units
5,167,034	N. H. MacLean, Jr., W. F. Micka, R. W. Miller, and M. R. Patel	Data Integrity for Compaction Devices
5,167,078	L. R. Bolde and J. S. Groman	Compact Photo Resist Dryer
5,167,513,	R. A. Johnson, H. B. Schwartz, P. Singh, and B. D. Washo	Load Limited Pin for an Electrical Connector
5,167,913	J. Acocella, P. L. Flaitz, R. N. Master, C. Narayan, S. H. Knickerbocker, P. H. Palmateer, S. Purushothaman, and S. S. N. Reddy	Method of Forming an Adherent Layer of Metallurgy on a Ceramic Substrate
5,168,167	R. Prakash	Optical Scanner Having Controllable Light Sources
5,168,216	T. Dance	System for Testing High-Speed Digital Circuits
5,168,245	G. N. Koskowich	Monolithic Digital Phaselock Loop Circuit Having an Expanded Pull-In Range
5,168,275	C. Harrison, M. D. Marik, and R. L. Posthumus	Method and Apparatus for Decoding Two Frequency (F/2F) Data Signals
5,168,347	J. B. Gillett	Integrated Circuit Chip Package Having Signal Input/Output Connections Located at Edges of the Substrate
5,168,348	R. C. Chu, M. J. Ellsworth, Jr., and D. T. Vader	Impingement Cooled Compliant Heat Sink
5,168,368	J. Gow III and R. W. Noth	Lead Frame-Chip Package with Improved Configuration
5,168,395	K. B. Klaassen and J. C. L. van Peppen	Controlled Magnetic Recording Head Relaxation in a Magnetic Recording System
5,168,413	J. D. Coker, R. L. Galbraith, P. P. Howard, G. J. Kerwin, and G. J. Smith	Transducer Head Flying Height Monitoring Methods and Apparatus for Disk Drive System
5,168,424	I. W. Bolton, F. E. Castle, and D. S. Gaunt	Multi Unit Electrical Apparatus with Dual Inlet Fan Positioned Opposite Unit Bays
5,168,440	J. H. Spreen	Transformer/Rectifier Assembly with a Figure Eight Secondary Structure
5,168,454	M. J. LaPlante, M. G. LaVine, D. C. Long, P. Lu, J. J. Seksinsky, L. D. Thorp, and G. Weiss	Formation of High Quality Patterns for Substrates and Apparatus Therefor

5,168,495	T. B. Smith	Nested Frame Communication Protocol
5,168,513	J. R. Maldonado and Y. Vladimirsky	X-ray Metrology and Alignment Detection System
5,168,554	C. A. Luke	Converting Trace Data from Processors Executing in Parallel into Graphical Form
5,168,571	R. D. Hoover, J. D. Irish, and D. W. Sollender	System for Aligning Bytes of Variable Multi-Bytes Length Operand Based on ALU Byte Length and a Number of Unprocessed Byte Data